

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Soltis, et al.

Confirmation No.:

Serial No.

Examiner:

Filing Date: August 27, 2003

Docket No. 10971190-3

Group Art Unit:

For: **PROCESSING SYSTEM AND METHOD FOR INDICATING INSTRUCTION ATTRIBUTE AND STATUS INFORMATION FOR LONG LATENCY INSTRUCTIONS**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

This information disclosure statement is filed in accordance with 37 C.F.R. §§ 1.56, 1.97, and 1.98. Listed on the accompanying form PTO-1449 are documents which may or may not be considered material to the examination of this application. These documents were made of record in a prior application filed January 28, 2000 and having serial no. 09/493,986, to which priority is claimed relative to the present application pursuant to 35 U.S.C. §120. Copies of these documents are not being provided pursuant to 37 C.F.R. § 1.98(d)(1) and (2), which provides:

(d) A copy of any patent, publication, pending U.S. application or other information, as specified in paragraph (a) of this section, listed in an information disclosure statement is required to be provided, even if the patent, publication, pending U.S. application or other information was previously submitted to, or cited by, the Office in an earlier application, unless:

- (1) The application is properly identified in the information disclosure statement and is relied on for an earlier effective filing date under 35 U.S.C. §120; and
- (2) The information disclosure statement submitted in the earlier application complies with a paragraphs (a) through (c) of this section.

The following rights are reserved by the Applicant(s): the right to establish the patentability of the claimed invention over any of the listed documents should they be applied as reference, the right to prove that some of these documents may not be prior art, and/or the right to prove that some of these documents may not be enabling for the teachings they purport to offer.

This statement should not be construed as a representation that an exhaustive search has been made, or that information more material to the examination of the present application does not exist. The Examiner is specifically requested not to rely solely on the materials submitted herewith. The Examiner is requested to conduct an independent and thorough review of the documents, and to form independent opinions as to their significance.

It is respectfully requested that the Examiner initial and return a copy of the enclosed PTO-1449 to indicate the documents have been considered.

Respectfully submitted,

**THOMAS, KAYDEN, HORSTEMEYER
& RISLEY, L.L.P.**

By:


Jon E. Holland, Reg. No. 41,077
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Intellectual Property Administration
P.O. Box 272400
Fort Collins, Colorado 80527-2400
Docket: 10971190-3
00054559.DOC

FORM PTO-1449		ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
		10971190-1		
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT		APPLICANT		
(Use several sheets if necessary)		Soltis, et al.		
		FILING DATE	GROUP	
		8/27/2003		

REFERENCE DESIGNATION**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
1A	5,859,999	01/12/1999	Morris, et al.	
1B	5,860,017	01/12/1999	Sharangpani, et al.	
1C	5,219,781	4/2001	Arora, Judge K.	
1D	4,903,196	2/1990	Pomerene, et al.	
1E	5,838,943	11/1998	Ramagopal, et al.	
1F	6,195,676	2/2001	Spix, et al.	
1G				
1H				
1I				
1J				
1K				

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
1L					
1M					
1N					
1O					
1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

1Q	Alexander Wolfe, "Patents Shed Light on Merced: Techniques of Predication and Speculation Detailed," Electronic Engineering Times, February 15, 1999, pp. 43-44.
1R	Patterson, et al., "Computer Architecture: A Quantitative Approach," Morgan Kaufmann Publishers, Inc., 2ed, pp.150-193.
1S	Gary Lauterbach, "Sun's Next-Generation High-End SPARC Microprocessor," Microprocessor Forum, October 14-15, 1997, pp. 3-6.

EXAMINER

DATE CONSIDERED